Abstract of the Disclosure

The present invention provides a refresh clock generator which optimally controls a period of a refresh clock signal according to temperature variation and outputs the refresh clock signal. The refresh clock generator includes a bias voltage generating unit for generating first and second bias voltages in response to a temperature variation and a clock generator for generating a refresh clock signal having a frequency which is controlled or adjusted based on the first and second bias voltages, wherein the first bias voltage is varied in proportion to the temperature variation; the second bias voltage is varied in inverse proportion to the temperature variation; and the frequency of the refresh clock signal is varied in proportion to the temperature variation.